



DYCONEX

an MST company

Advanced Rigid Substrates for HF Applications, Ultra-thin Build-ups and Chip Packages

HIGHLIGHTS

- Epoxy based laminates with low dielectric properties
- High Tg, low CTE and high dielectric strength
- Ultra-thin any layer HDIs from 4 layers (~220 μm) to 8 layers (~350 μm) with highest reliability
- Impedance controlled ultra-fine line technology (down to 25 μm)
- High density pitch design (~175 μm)
- Smallest via (50 μm) in pad (100 μm)
- EN 9100:2009 certification



Micro Systems Technologies
engineering for life

Advanced Rigid Substrates for HF Applications, Ultra-thin Build-ups and Chip Packages

DYCONEX features an advanced rigid multilayer material allowing ultra-thin build-ups for various high frequency applications particularly suitable for HF chip packaging. The material offers low dielectric properties (low Dk and low Df) combined with a low thermal expansion coefficient (CTE) and high dielectric strength. Using anylayer technology the layers of the substrates are only connected by laser drilled, copper filled microvias allowing for high-density / high-reliability designs. Other HDI build-up solutions are available in addition.

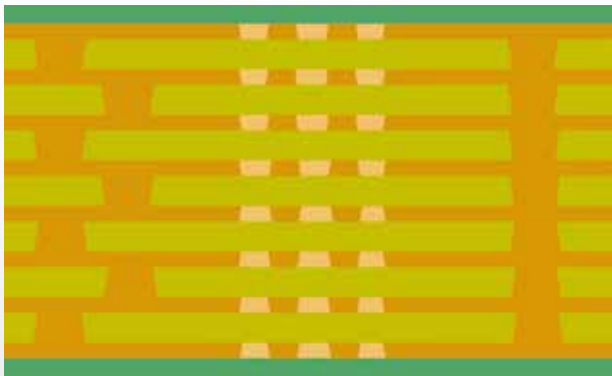
MATERIAL CHARACTERISTICS

Characteristic		Unit	Conditions
CTE	9 - 11	ppm/°C	XY <Tg
	20 - 25	ppm/°C	Z <Tg
Tg	260 - 280	°C	DMA
Flexural modulus	25	GPa	25°C
Dielectrical constant	3.5	ϵ_r	1 GHz
	3.4	ϵ_r	10 GHz
Dissipation factor	0.003		1 GHz
	0.004		10 GHz
Dielectrical strength	160	kV/mm	ASTM D149 Method A

DESIGN FEATURES

Feature	Standard capability	Leading edge capability
Min laser blind via	75 μm	50 μm
Min pad size	150 μm	100 μm
Min pitch	250 μm	175 μm
Copper filled blind vias	all	all
Filling rate blind via	>90 %	>95 %
Final copper thickness	20 - 40 μm	10 - 20 μm
Min lines / spaces	50 - 70 μm	25 - 35 μm
Solder mask thickness	20 \pm 10 μm	18 \pm 4 μm
Solder mask registration accuracy	\pm 25 μm	\pm 15 μm
Min solder mask dam	90 μm	75 μm
Solder mask color	green / black	green / black
Silkscreen	white / black	white / black
Surfaces	ENIG	ENEPIG/OSP/ISIG/EPIG
Routing	2 mm	0.6 mm

ANYLAYER BUILD-UP



■ Solder mask ■ Metal foil: Copper ■ Prepreg: Polyimide glass

THICKNESS OF ULTRA-THIN RIGID BUILD-UPS

Number of layers	Standard capability	Leading edge capability
2	150 \pm 20 μm	100 \pm 20 μm
4	250 \pm 25 μm	200 \pm 20 μm
6	340 \pm 30 μm	250 \pm 25 μm
8	430 \pm 40 μm	300 \pm 30 μm
>8	on request	on request

Thickness measured over all layers incl. solder mask



an MST company

DYCONEX AG
Grindelstrasse 40
CH - 8303 Bassersdorf
Switzerland
Phone +41 (43) 266 11 00
mail.dyconex@mst.com
www.mst.com/dyconex

Based in Switzerland, DYCONEX has been in the PCB business for more than 50 years and delivers leading-edge interconnect solutions in flex, rigid-flex and rigid technology. DYCONEX core competence lies in the production of highly complex HDI, high-frequency and high-reliability PCBs for medical, aerospace, defense, industrial and semiconductor applications. DYCONEX is EN 9100, ISO 9001 and ISO 13485 certified and a company of the MST group.



Micro Systems Technologies
engineering for life

Micro Systems Technologies
Neuhofstrasse 4
CH - 6340 Baar
Switzerland
Phone +41 (44) 804 63 00
info@mst.com
www.mst.com